



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/551,266 Group Art Unit: 2816
Filing Date: September 29, 2005 Conf.: 3385
Applicant: Toshiro AKINO Examiner: O'Toole, Colleen J.
Title: LATERAL BIPOLAR CMOS INTEGRATED CIRCUIT
Attorney Docket: 9694D-000025/US

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November 4, 2009

AMENDMENT UNDER 37 CFR §1.114

Sir:

In response to the Examiner's Final Office Action mailed May 6, 2009, the Advisory Action mailed October 19, 2009, the Examiner Interview conducted October 29, 2009, and concurrent with the filing of a Request for Continued Examination (RCE), the shortened statutory period for reply having been extended by three (3) months to November 6, 2009 by a Petition for Extension of Time filed herewith, the following amendments and remarks are respectfully submitted.

Amendments to the Claims begin on page 2 of this Amendment,

Remarks begin on page 6 of this Amendment.

	CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NUMBER PREVIOUSLY PAID FOR		PRESENT EXTRA
TOTAL	8	-	20	=	0
INDEPENDENT	1	-	3	=	0